

Typical Features

- ◆ Fixed input voltage, Isolated & unregulated output, 2W
- ◆ High Efficiency up to 87%
- ◆ Small compact SIP packing
- ◆ No external component required
- ◆ Isolation Voltage 3000VDC
- ◆ Operating Temperature: -40℃~+85℃
- ◆ Plastic Case, meet UL94 V-0 standard



Test Condition: Unless otherwise specified, data in the datasheet should be tested under the conditions of inputting nominal voltage, pure resistance rated load and Ta=25℃

Application Field

It could be widely used for instrument, communication, pure digital circuit, general low frequency analog circuit, relay drive circuit, data exchange circuit, etc.

Typical Product List

Model	Input Voltage Range (VDC)		Output Voltage/Current (Vo/Io)		Input Current(mA) Nominal Voltage		Max. Capacitive Load	Ripple & Noise (Max.)	Efficiency (%)full load, input nominal voltage	
	Nominal	Range	Voltage (VDC)	Current(mA) MAX./Min.	Full load Typ.	No Load Typ.	uF	mVp-p	Min.	Typ.
FN2-05S3V3C3	5	4.5 - 5.5	3.3	600	533	50	2000	150	73	75
FN2-05S05C3			5	400	519	50	2000	150	75	77
FN2-05S09C3			9	220	513	50	2000	150	76	78
FN2-05S12C3			12	167	513	50	2000	150	76	78
FN2-05S15C3			15	133	506	50	2000	150	77	79
FN2-05S24C3			24	83	506	50	2000	150	77	79
FN2-12S3V3C3	12	10.8 - 13.2	3.3	600	222	18	2000	150	73	75
FN2-12S05C3			5	400	195	18	2000	150	82	84
FN2-12S09C3			9	220	211	18	2000	150	77	79
FN2-12S12C3			12	167	186	18	2000	150	85	87
FN2-12S15C3			15	133	216	18	2000	150	75	77
FN2-12S24C3			24	83	184	18	2000	150	84	86
FN2-15S3V3C3	15	13.5 - 16.5	3.3	600	178	15	2000	150	73	75
FN2-15S05C3			5	400	173	15	2000	150	75	77
FN2-15S09C3			9	220	173	15	2000	150	75	77
FN2-15S12C3			12	167	171	15	2000	150	76	78

FN2-15S15C3			15	133	171	15	2000	150	76	78
FN2-15S24C3			24	83	169	15	2000	150	77	79
FN2-24S3V3C3	24	21.6 - 26.4	3.3	600	113	10	2000	150	73	74
FN2-24S05C3			5	400	102	10	2000	150	79	81
FN2-24S09C3			9	220	95	10	2000	150	84	86
FN2-24S12C3			12	167	96	10	2000	150	85	87
FN2-24S15C3			15	133	105	10	2000	150	77	79
FN2-24S24C3			24	83	96	10	2000	150	84	86
FN2-05D05C3	5	4.5 - 5.5	±5	±200	519	50	2000	150	75	77
FN2-05D09C3			±9	±110	513	50	2000	150	76	78
FN2-05D12C3			±12	±83	513	50	2000	150	76	78
FN2-05D15C3			±15	±67	506	50	2000	150	77	79
FN2-12D05C3	12	10.8 - 13.2	±5	±200	214	18	2000	150	76	78
FN2-12D09C3			±9	±110	211	18	2000	150	77	79
FN2-12D12C3			±12	±83	211	18	2000	150	77	79
FN2-12D15C3			±15	±67	216	18	2000	150	75	77
FN2-24D05C3	24	21.6 - 26.4	±5	±200	108	18	2000	150	75	77
FN2-24D09C3			±9	±110	107	18	2000	150	83	85
FN2-24D12C3			±12	±83	105	18	2000	150	84	86
FN2-24D15C3			±15	±67	97	11	2000	150	85	87

Note:

1. “*” are models under developing.
2. In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance recommended equal to 10% nominal power.
3. The capacitive loads of positive and negative outputs are identical;

Input Specifications

Item	Working Conditions	Min.	Typ.	Max.	Unit
Input Overshoot Voltage (1 Second.max.)	5Vdc Input	-0.7	--	9	Vdc
	9Vdc Input	-0.7	--	15	
	12Vdc Input	-0.7	--	18	
	15Vdc Input	-0.7	--	21	
	24Vdc Input	-0.7	--	30	
Input Filter	Capacitor Filter				

Output Specifications

Item	Working Conditions	Min.	Typ.	Max.	Unit
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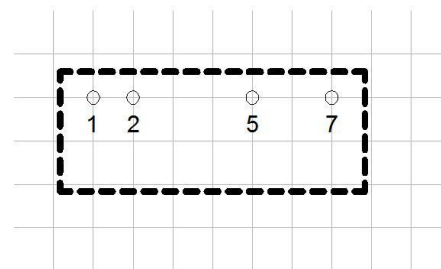
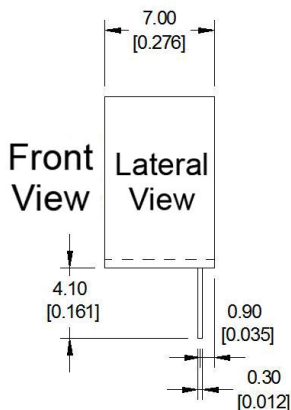
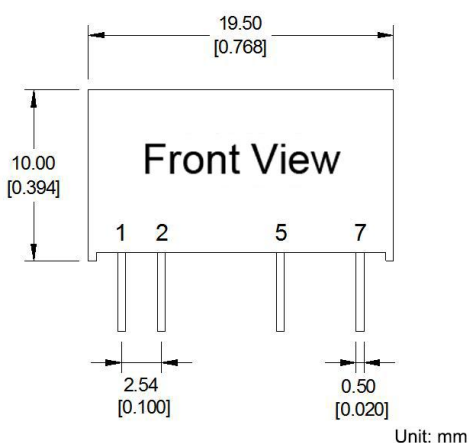
Output Power			0.2	--	2	W
Output Voltage Accuracy	Nominal input, Full load		--	±2	±5	%
Load Regulation	10% ~ 100% nominal load	3.3Vdc output	--	--	20	
		Other output	--	--	15	
Line Voltage Regulation	Input Voltage Change±1%	3.3Vdc output	--	--	±1.5	
		Other output	--	--	±1.2	
Ripple & Noise①	Nominal input, full load	Other output	--	100	150	mVp-p
Temperature Drift Coefficient	100% Full Load		--	--	±0.03	%/°C
Output Short Circuit Protection ②			Not Available			

NOTE:①Ripple & Noise tested by twisted-pair method.

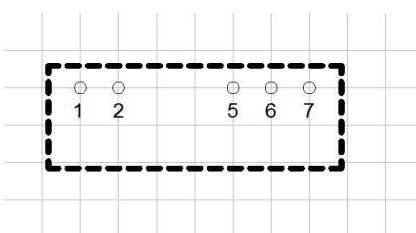
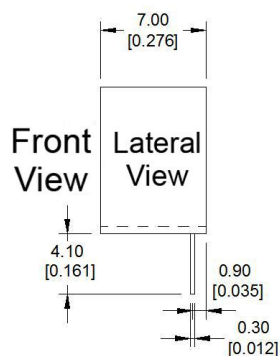
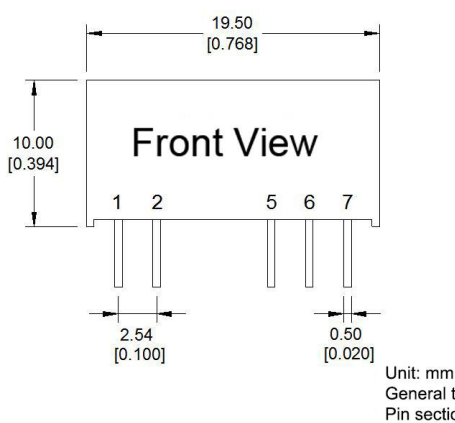
General Specifications

Switching Frequency	Typical	100KHz (Typ.)
Operating Temperature	Refer to Temperature Derating Curve	-40°C ~ +85°C
Storage Temperature		-55°C ~ +125°C
Shell temperature rise during work	Within Temperature Derating Curve	25°C(Typ.)
Relative Humidity	No condensing	5%~95%
Case Material		Black flame-retardant heat-resistant Plastic(UL94 V-0)
Pin Withstand Soldering Temp	Distance to Case 1.5mm, 10S	300°C MAX
Isolation Voltage	Test 1 minute, leakage current< 0.5mA	3000Vdc
Isolation Capacitor	Input/Output,100KHz/0.1V	20 pF (Typ.)
MTBF	MIL-HDBK-217F@25°C	35X10 ⁵ Hrs
Product Weight		2.5g (Typ.)
Packing	Tube(525*18*10mm)	25PCS
	Box(542*110*155mm)	2000PCS(Total 80tubes)

Packing Dimension



Print board vertical view
Grid: 2.54mm(0.1inch)



Print board vertical view
Grid: 2.54mm(0.1inch)

Unit: mm
General tolerance: 0.xx±0.1(0.xx±0.004)
Pin section tolerances: ±0.25(0.xx±0.01)

Packing Code	L x W x H	
C	19.50×7.00 × 10.00mm	0.768 × 0.276 × 0.394inch

Pin Function

Pin Function	1	2	3, 4	5	6	7
Single (S)	+Vin	GND	NP	-Vo	NP	+Vo
Dual (D)	+Vin	GND	NP	-Vo	COM	+Vo

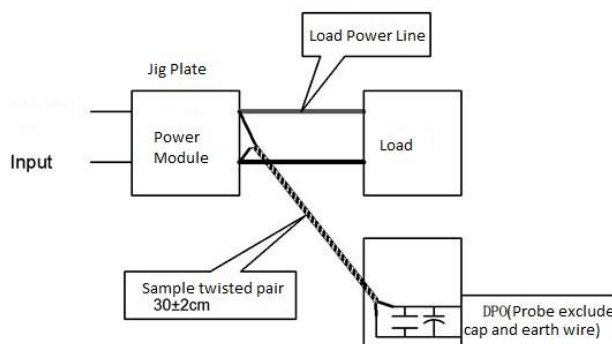
Note: if the definition of pin is not in accordance with the model selection manual, please refer to the label on actual item.

Ripple& Noise Test: (Twisted Pair Method 20MHZ bandwidth)

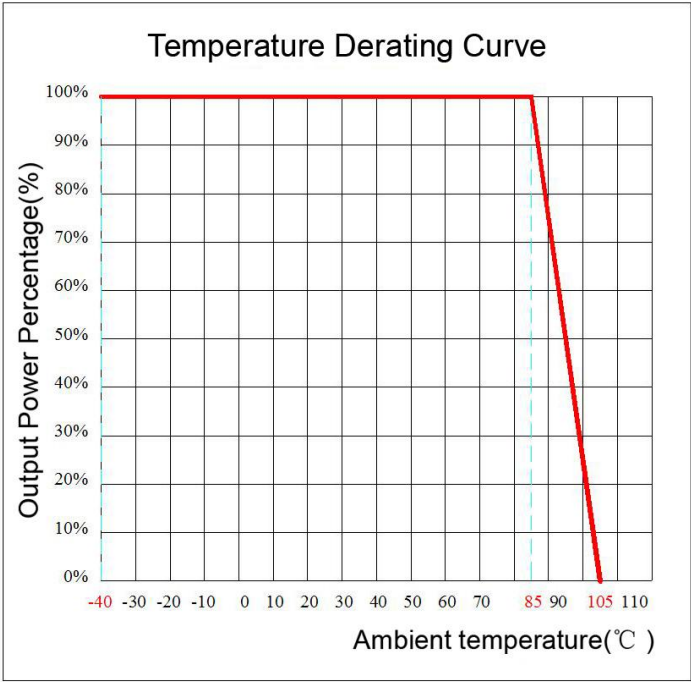
Test Method:

a. 12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 10uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.

b. Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm±2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.

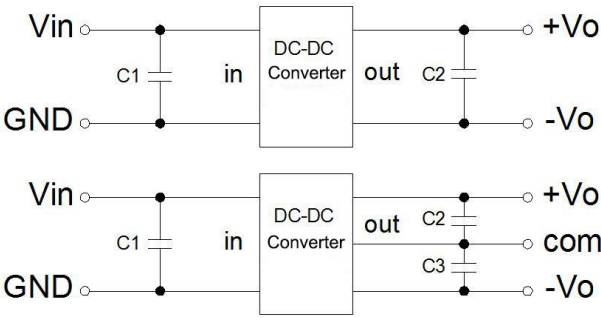


Temperature Curve



Design and Application Circuit Recommended

1. Output load requirements
- a. In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance equal to 10% nominal load.
 - b. The maximum capacitive load is tested under nominal input full load, and cannot exceed the maximum capacitive load of output terminal under operation, otherwise it will cause it difficult to start up and damage the product.
2. Recommended circuit
- In order to ensure the input/output ripple and noise decreased, capacitor filter net could be connected to input and output terminal, application circuit as below photo 1; choosing suitable filter capacitor is very important, start-up problems may be caused by too large capacitance. To ensure the modules running safely and reliably, the recommended capacitive load values as shown in Table 1. (But for the actual output power of application circuit is less than 0.5W, suggest not to connect external capacitor)



Recommended capacitive load value(Table 1)

Vin (Vdc)	C1 (μF)	Vout (Vdc)	C2 (μF)	Vout (Vdc)	C2,C3 (μF)
3.3/5	4.7	3.3/5	10	±3.3/±5	4.7
12	2.2	9	4.7	±9	2.2
15	1	12	2.2	±12	1
24	1	15	1	±15	0.47
--	--	24	0.47	±24	0.22

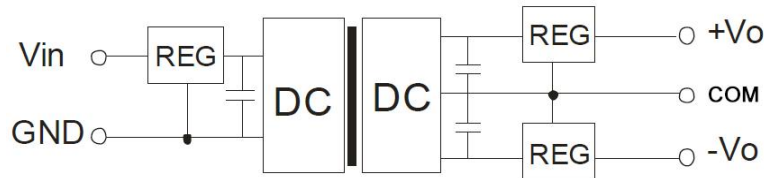
3. Output regulated voltage and over voltage protection circuit

The simplest device to protect output regulated voltage, over voltage and over current is to cascade a linear regulator with overheat protection at input or output terminal, and connect a capacitor filter net(see below picture), filter capacitive value recommended see table 1, Linear regulator is chosen according to the actual voltage, current needed in working, or choose our NW series products.

Single Output



Positive Negative Dual Output



Note:

- 1.This product cannot be used in parallel, and do not support hot-plugging;
- 2.If the product works below the minimum required load, it cannot guarantee that the product performance meets all performance indicators in this manual;
3. All index testing methods in this datasheet are based on our Company's corporate standards
4. The product specification may be changed at any time without prior notice.

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